## Refine Search

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SCHEDUL\$5	0
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SCHEDULAR	158
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(SCHEDUL\$5 NEAR12 (BREAK\$6 OR STOP\$4 OR INHIBIT\$4 OR HALT\$4 OR SUSPENSION\$1 OR SUSPEND\$3) NEAR45 PIPELIN\$7).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	44

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DATE: Monday,	November 20, 2006	<u>Purge (</u>	<u>Queries</u>	Printable Copy	<u>Create Case</u>	Set
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DB=	=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L44</u>	schedul\$5 near12 (break\$6 or stop\$4 or inhibit\$4 or halt\$4 or suspension\$1 or suspend\$3) near45 pipelin\$7	44	<u>L44</u>
<u>L43</u>	141 and 15	48	L43
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<u>L41</u>	(schedul\$5 or arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3 or break\$7) near17 pipelin\$6	334	<u>L41</u>
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<u>L34</u>	L30 and l21	8	<u>L34</u>
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<u>L32</u>	L30 and 15	45	<u>L32</u>
<u>L31</u>	L30 and 14	57	<u>L31</u>
<u>L30</u>	(schedul\$5 or arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	266	<u>L30</u>
DB=	=PGPB,USPT; PLUR=YES; OP=OR		
<u>L29</u>	120 and 16	0	<u>L29</u>
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<u>L27</u>	120 and 14	32	<u>L27</u>
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<u>L25</u>	120 and 122	7	L25
<u>L24</u>	120 and 121	7	<u>L24</u>
<u>L23</u>	(717/124-127, 140-167)![CCLS]	1759	<u>L23</u>
<u>L22</u>	(714/10-57)[CCLS]	13657	<u>L22</u>
<u>L21</u>	(712/242-248)![CCLS]	1467	<u>L21</u>
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L20</u>	(arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	211	<u>L20</u>
<u>L19</u>	115 not 116	- 57	<u>L19</u>
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<u>L17</u>	L15 and 15	. 6	<u>L17</u>
<u>L16</u>	L15 and 14	13	L16
<u>L15</u>	(arbitrar\$3 or random\$3 or uncondition\$3 or user) near12 (suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	70	<u>L15</u>
<u>L14</u>	111 not 17	2	L14

<u>L13</u>	L10 and 16	1	<u>L13</u>
<u>L12</u>	L10 and 15 .	11	<u>L12</u>
<u>L11</u>	L10 and 14	12	<u>L11</u>
<u>L10</u>	(forc\$4 or uncondition\$3 or overrid\$3) near12 (suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	178	<u>L10</u>
DB=	=PGPB,USPT; PLUR=YES; OP=OR		
<u>L9</u>	13 and 16	1	<u>L9</u>
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DB=	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		
<u>L3</u>	(forc\$4 or uncondition\$3 or overrid\$3) near12 (inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	127	<u>L3</u>
<u>L2</u>	uncondition\$3 near12 (inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	0	<u>L2</u>
<u>L1</u>	uncondition\$3 near5 (inhibit\$4 or stop\$4 or halt\$3) near8 pipelin\$6	0	<u>L1</u>

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Results for "((pipelin\* <and> (stop\*, inhibit\*, halt\*, suspend\*, suspension\*) <near/12> (stage\*, c..." ☑ e-mail Your search matched 3 of 1432467 documents. A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order. a Search Options View Session History Modify Search ((pipelin\* <and> (stop\*, inhibit\*, halt\*, suspend\*, suspension\*) <near/12> (stage\*, cy New Search Check to search only within this results set » Key Display Format: Citation Citation & Abstract IEEE JNL IEEE Journal or Magazine IEE JNL IEE Journal or Magazine view selected items Select All Deselect All HEEE CNF IEEE Conference Proceeding IEE CNF IEE Conference Proceeding 1. Detection of obstacles in the flight path of an aircraft Gandhi, T.; Mau-Tsuen Yang; Kasturi, R.; Camps, O.; Coraor, L.; McCandless, J.; BEER STO IEEE Standard Aerospace and Electronic Systems, IEEE Transactions on Volume 39, Issue 1, Jan. 2003 Page(s):176 - 191 Digital Object Identifier 10.1109/TAES.2003.1188902 AbstractPlus | Full Text: PDF(1126 KB) | HEEE JNL Rights and Permissions 2. A Hardware IP-Core for Information Retrieval Freeman, M.; Jayasooriya, T.; Digital System Design: Architectures, Methods and Tools, 2006, DSD 2006, 9th EUROMICRO Con 30-01 Aug. 2006 Page(s):115 - 122 Digital Object Identifier 10.1109/DSD.2006.8 AbstractPlus | Full Text: PDE(288 KB) ISES CNF Rights and Permissions 3. Stage-skip pipeline: a low power processor architecture using a decoded instruction buffer Hiraki, M.; Bajwa, R.S.; Kojima, H.; Gorny, D.J.; Nitta, K.; Shri, A.; Low Power Electronics and Design, 1996. International Symposium on 12-14 Aug. 1996 Page(s):353 - 358 Digital Object Identifier 10.1109/LPE.1996.547538 AbstractPlus | Full Text: PDF(560 KB) ISSE CRE Rights and Permissions

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